mm-Wave High Gain Cavity-Backed Aperture-Coupled Patch Antenna Array

ZHU, Jianfeng; CHU, Chenhao; DENG, Li; ZHANG, Chen; YANG, Yang; LI, Shufang

Published in:
IEEE Access

Published: 01/01/2018

Document Version:
Final Published version, also known as Publisher's PDF, Publisher's Final version or Version of Record

Publication record in CityU Scholars:
Go to record

Published version (DOI):
10.1109/ACCESS.2018.2859835

Publication details:

Citing this paper
Please note that where the full-text provided on CityU Scholars is the Post-print version (also known as Accepted Author Manuscript, Peer-reviewed or Author Final version), it may differ from the Final Published version. When citing, ensure that you check and use the publisher's definitive version for pagination and other details.

General rights
Copyright for the publications made accessible via the CityU Scholars portal is retained by the author(s) and/or other copyright owners and it is a condition of accessing these publications that users recognise and abide by the legal requirements associated with these rights. Users may not further distribute the material or use it for any profit-making activity or commercial gain.

Publisher permission
Permission for previously published items are in accordance with publisher's copyright policies sourced from the SHERPA RoMEO database. Links to full text versions (either Published or Post-print) are only available if corresponding publishers allow open access.

Take down policy
Contact lbscholars@cityu.edu.hk if you believe that this document breaches copyright and provide us with details. We will remove access to the work immediately and investigate your claim.

Download date: 22/12/2022
mm-Wave High Gain Cavity-Backed Aperture-Coupled Patch Antenna Array

JIANFENG ZHU, CHEN-HAO CHU, LI DENG, CHEN ZHANG, YANG YANG, (Senior Member, IEEE), AND SHUFANG LI, (Senior Member, IEEE)

1Beijing Key Laboratory of Network System Architecture and Convergence, Beijing University of Posts and Telecommunications, Beijing 100876, China
2State Key Laboratory of Millimeter Waves, Electronic Engineering Department, City University of Hong Kong, Hong Kong
3School of Electrical and Data Engineering, University of Technology Sydney, Sydney, NSW 2007, Australia

Corresponding author: Shufang Li (bupt_paper@126.com)

This work was supported in part by the National Natural Science Foundation of China under Grant 61427801 and Grant 61601040, in part by the 111 Project under Grant B17007, and in part by the Beijing Nova Program under Grant Z181100006218039.

ABSTRACT A wideband and high gain cavity-backed 4 × 4 patch antenna array is proposed in this paper. Each patch antenna element of the array is enclosed by a rectangular cavity and differentially-fed by the slot underneath. By optimizing the geometry of the radiating patch and the cavity, a very uniform E-field distribution at the antenna aperture is achieved, leading to the high array aperture efficiency and thus the gain. Taking advantages of the higher-order substrate integrated cavity excitation, the elements of the array are efficiently fed with the same amplitude and phase in a simplified feeding mechanism instead of the conventional bulky and lossy power-splitter-based feeding network. Measured results show the antenna bandwidth is from 56 to 63.1-GHz (16.1%) with the peak gain reaching 21.4 dBi. The radiation patterns of the array are very stable over the entire frequency band and the cross-polarizations are as low as −30 dB. These good characteristics demonstrate that the proposed array can be a good candidate for the future 60-GHz communication system applications.

INDEX TERMS mm-wave, cavity-backed, aperture coupled, substrate integrated waveguide, array antenna, higher order mode.

I. INTRODUCTION

With the unprecedented rapid development of the fifth generation (5G) wireless communications in recent years, mm-Wave frequency band, the 60-GHz band in particular, has emerged as one of the most promising candidates for the multi-gigabit wireless indoor communication systems. Due to the continues and the sufficient bandwidth, the communication systems at 60-GHz are capable of achieving a high data rate up to multiple gigabits per seconds, which opens the door to the future wireless data transfer, particularly for the transmission of the uncompressed high-definition video and ultra-fast file [1], [2]. As a vital portion of the communication systems, mm-Wave antennas or arrays that feature low cost, wideband and high gain are in increasing demand. To date, various types of antennas with good performances for mm-Wave applications have been proposed [3]–[29], such as patch antennas [3]–[7], grid antennas [8]–[11], aperture antennas [12]–[14], dipole antennas [15], [16], slot antennas [17]–[23], and cavity-backed antennas [24]–[31]. Although the 60-GHz technology offers various advantages over currently proposed or existing communications systems, it has the disadvantage of the lossy channel with the excess loss approximately up to 15dB/km due to oxygen absorption [32]. Therefore, increasing the transmitter or receiver antenna gain is not only desirable but inevitable to compensate the significant propagation loss caused by the oxygen absorption and ensure that a sufficient margin exists to overcome other loss, such as rain-induced fading. Generally, there are two approaches to increase the antenna gain. One is increasing the radiating aperture size of the antenna while the other is improving the antenna aperture efficiency. The latter is more favorable as antennas with large physical size are usually cost-ineffective and bulky in structure, which is difficult to be integrated into the frontend of the transceiver. To improve the aperture efficiency, cavity-backed antennas have been demonstrated an efficient approach [33]–[35]. By optimizing the cavity geometry and size, a very uniform E-field distribution can be achieved.
FIGURE 1. 3-D view of the proposed 4 × 4 antenna array.

at the aperture, leading to a high aperture efficiency [13], [36]–[38]. Moreover, the cavity can suppress the surface wave propagate along the substrate and also reduce the mutual coupling between the adjacent radiating elements of the array, which leads to a high radiation efficiency and stable radiation pattern over the wide frequency bandwidth.

Besides antenna gain and efficiency, the effect of the fabrication tolerance on antenna performance should not be neglected because of the very small wavelengths. Therefore, a highly accurate fabrication technology is required. Otherwise, the antenna performance including matching, gain as well as efficiency will be deteriorated. To alleviate the fabrication tolerance on the antenna performance, higher-order-mode substrate integrated cavity (SIC) excitation was proposed in [39] and [40] instead of other complicated feed networks to reduce the number of metal posts in the cavity for feeding elements of the antenna array. Nevertheless, their impedance bandwidths can be further improved for widespread applications.

In this paper, a new wideband cavity-backed patch antenna array with very high aperture efficiency as well as the gain is proposed for the 60-GHz applications. Firstly, a 2 × 2-element cavity-backed aperture-coupled patch antenna array is demonstrated, which exhibits high aperture efficiency as well as gain. Taking advantages of the higher-order-mode cavity excitation, the elements of the array are efficiently excited with the same amplitude and phase in a simple TE_{340} mode cavity, which eases the burdens of the conventional bulky and lossy feednetwork containing multiple power splitters and SIW-lines. The higher-order-mode cavity resonance is excited by a simple slot aperture located in the bottom center of the cavity. Then, the 2 × 2-element patch array is used as subarray to built a 4 × 4-element array, as shown in Fig. 1. Measured results show the antenna bandwidth is from 56 to 63.1-GHz (16.1%) with peak gain reaching 21.4 dBi. The radiation patterns of the array are very stable over the entire frequency band and the cross-polarizations are as low as −30 dB. These good characteristics demonstrate the proposed array can be a good candidate for the future 60-GHz communication systems.

II. THE 2 × 2-ELEMENT SIC-EXCITED ARRAY

The geometry of the proposed 2 × 2-element cavity-backed patch antenna subarray is shown in Fig. 2. The array consists of three Rogers 5880 layers with a dielectric constant of 2.2 and thickness of 0.787 mm. The 2 × 2 cross-shaped radiating patch antenna array incorporated with its rectangular cavity is located in the upper substrate (D_1). Each cross-shaped patch is differentially fed by the coupling slot right underneath the patch. The coupling slot is cut on the top wall
of the substrate integrated cavity (SIC), which is implemented on the middle substrate (D2) with metallized vias as sidewalls and metals as its top and bottom. The cavity is excited by the center slot in the bottom plane and the slot is cut on the top wall of the lower SIW lines. In order to obtain enough space to arrange the four cavity-backed antenna elements while maintaining a compact size simultaneously, the size of the SIC is adopted as 8.5 mm × 8.5 mm. In this case, the TE340 mode is excited in the cavity. The complex E-field distributions in the cavity at 57, 60, and 63 GHz are shown in Fig. 3. It is seen that the TE340 mode exists over the operating frequency band although the mode slightly deteriorates as the frequency varies. In fact, the radiation performance of the array will not be affected by the mode deterioration due to the fact that the slots cut on the top of the cavity are symmetrical along the center plane, and thus the radiating elements are always excited with the same phase and amplitude.

To evaluate the performance of the proposed subarray, the simulated gains is shown in Fig. 4. It is observed that the gains are escalated from 14.3 to 15.3 dBi with its peak occurring at 60 GHz. In fact, this high gain is achieved due to the uniform E-field distributions at the antenna aperture, which can be easily realized by optimizing the cavity size and the patch geometry that enclosed. To better demonstrate this, the E-field distributions at the top surface of the antenna aperture are shown in Fig. 5. A very uniform E-field distributions can be observed in each cavity, indicating that the high aperture efficiency as well as the gain can be achieved.

### III. THE 4 × 4-ELEMENT SIC-EXCITED ARRAY

Based on the proposed 2 × 2 cavity-backed subarray, the 4 × 4-element array is proposed and verified, as shown in Fig. 1. 4 × 4 cross-shaped radiating patch array together with its cavity is implemented on the upper layer (D1). They are excited by the four substrate integrated cavities that are realized in the middle substrate (D2). The 1-to-4 SIW-based power splitter is implemented on the lower layer (D3), which is directly fed at the center by the WR-15 waveguide. The distances between the adjacent subarrays are 8.5 mm in both the x-direction and y-direction. Top view of each layer of the array is shown in Fig. 6 and the detailed dimensions of the proposed 4 × 4 array are given in Table 1.
A. WAVEGUIDE-FED 1-TO-4 POWER SPLITTER

The amplitude and phase balance of the 1-to-4 power splitter are crucial to the antenna performance. Therefore, the proposed differentially-fed 1-to-4 power splitter is carefully designed and evaluated, as shown in Fig. 7. The simulated performance of the 1-to-4 power divider together with the WR15-to-SIW transition is given in Fig. 8. It is seen that the $S_{11}$ is below $-15$ dB from 57 to 64-GHz. The energy is equally divided into the four output ports and the phase imbalance is less than 0.5°, indicating the good performance of the proposed waveguide-fed 1-to-4 power splitter.

B. EXPERIMENTAL RESULTS

A prototype is fabricated and measured to verify the design, as shown in Fig. 9. Three Rogers 5880 laminates are fabricated independently and then stacked together by screw holes. Thermally & Electrically Conductive Adhesive (TECA) films are used to bond the dielectric layers and wipe out the possible air between them. The reflection coefficient of the array, which is measured by a Vector Network Analyzer MS4646B, is shown in Fig. 10. The simulated $-10$ dB bandwidth is from 57 to 64.2-GHz (17.7%) and the measured result is from 56 to 63.1-GHz (16.1%). The antenna gain versus frequency is shown in Fig. 11. The gains are escalated from 19.6 to 21.6 dBi for simulation and from 19.9 to 21.4 dBi for measurement. The formula for calculating aperture efficiency...
TABLE 2. Comparison of Different arrays working at 60 GHz.

<table>
<thead>
<tr>
<th>Ref.</th>
<th>Antenna Type</th>
<th>Feeding Network</th>
<th>No. of layer</th>
<th>Volume (length × width ×height) (mm³)</th>
<th>-10dB Impedance Bandwidth</th>
<th>Peak Gain (dBI)</th>
<th>Aperture efficiency</th>
<th>Radiation efficiency</th>
</tr>
</thead>
<tbody>
<tr>
<td>[3]</td>
<td>L-probe patch(LTCC)</td>
<td>stripline</td>
<td>10</td>
<td>14.1×14.4×1 (2.82×2.88×0.28a₀)</td>
<td>29%</td>
<td>17.5</td>
<td>Not given</td>
<td>&gt;80%</td>
</tr>
<tr>
<td>[4]</td>
<td>Patch array</td>
<td>CPW</td>
<td>1</td>
<td>17×17×3.8 (3.4×3.4×0.76a₀)</td>
<td>25.5%</td>
<td>16.7</td>
<td>Not given</td>
<td>Not given</td>
</tr>
<tr>
<td>[8]</td>
<td>Grid array</td>
<td>Microstrip line</td>
<td>6</td>
<td>15×15×0.6 (3.0×3.0×0.12a₀)</td>
<td>11.5</td>
<td>17.7</td>
<td>59.9%</td>
<td>Not given</td>
</tr>
<tr>
<td>[21]</td>
<td>Slot (PCB) (Series)</td>
<td>SIW</td>
<td>1</td>
<td>30.7×30.7×0.508 (6.1×6.1×0.13a₀)</td>
<td>4.1%</td>
<td>22</td>
<td>41.2%</td>
<td>68%</td>
</tr>
<tr>
<td>[25]</td>
<td>Slot antenna (PCB)</td>
<td>SIW (Parallel)</td>
<td>1</td>
<td>14×13.5×0.635 (2.8×2.7×0.13a₀)</td>
<td>11.6%</td>
<td>12.2</td>
<td>Not given</td>
<td>Not given</td>
</tr>
<tr>
<td>[28]</td>
<td>Cavity (LTCC) (PCB)</td>
<td>SIW (Parallel)</td>
<td>20</td>
<td>24.6×31×2.0 (4.9×6.2×0.43a₀)</td>
<td>17.1%</td>
<td>22.1</td>
<td>42.3%</td>
<td>44.4%</td>
</tr>
<tr>
<td>[30]</td>
<td>Cavity-backed patch</td>
<td>SIW (Parallel)</td>
<td>4</td>
<td>16.3×17.1×2.3 (3.26×3.42×0.46a₀)</td>
<td>22.6%</td>
<td>19.6</td>
<td>80%</td>
<td>54.5%</td>
</tr>
<tr>
<td></td>
<td>antenna(PCB)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>This work</td>
<td>Cavity-backed patch</td>
<td>SIW (higher-order-mode)</td>
<td>3</td>
<td>30×30×2.4 (6.0×6.0×0.48a₀)</td>
<td>&gt;18.2%</td>
<td>21.4</td>
<td>70.3%</td>
<td>95%(Sim)</td>
</tr>
</tbody>
</table>

where \( G \) and \( A_p \) are the gain and the physical aperture of the antenna, respectively. Considering that the relatively large area (30 × 30 mm²) of the proposed array is due to the peripheral area occupied by the screw holes and this peripheral area contributes little to the antenna gain, we simulated the antenna boresight gain with dielectric margins that are occupied by the screw hole removed, as shown in Fig. 11. Under this circumstance, the size of the physical radiating area is 20 mm × 20 mm and the calculated aperture efficiency is about 70.3% at 61 GHz, which is acceptable for the array antenna.

The radiation patterns of the array at 57, 60 and 63-GHz are given in Fig. 12. The patterns are measured by using the far-field mm-Wave measurement system. Due to the system limitation, only half of the sphere is measured. It is seen the patterns are generally symmetrical with its main beam and the highest gain fixed at its boresight. Cross polarizations...
FIGURE 10. Simulated and measured reflection coefficient.

FIGURE 11. Simulated and measured antenna gains.

are lower than $-40$ dB and $-30$ dB for the simulation and measurement, respectively. The sidelobes are generally below $-12$ dB at both planes. The discrepancy between the simulation and measurement is due to the fabrication tolerance and other uncertainties such as dielectric change of the substrate, etc.

The simulated radiation patterns and the gain of the array without the top patch are also given in the Fig. 13 and 14, respectively. As can be seen, the side lobes of the radiation patterns deteriorate at high frequencies. At 60 and 63-GHz, the side lobe levels are worse than $-10$ dB. The gain of the array also shows about 1.6 to 2 dB drop over the frequency band compared with the proposed design. Therefore, we can conclude that adding the top patch can help to improve the radiation performance of the array through properly adjusting the geometry of the patch.

C. DISCUSSION

Table 2 compares the key characteristics of the proposed antenna array with other 60-GHz antenna arrays. Although patch antenna array with L-probe feed [3], CPW feed [4] and grid array antenna with microstrip feed [8] show many excellent performances including wide impedance matching and good radiation patterns. The aperture efficiency and the gain are lower than the proposed one. Besides, CPW and microstrip line are relatively lossier in 60-GHz frequency band compared with SIW feeding, especially at the discontinuities. In order to obtain high aperture efficiency as well as high gain, patch antennas that are enclosed by the cavity are used in many array designs [21], [25], [29], [30]. Although these designs can achieve very good performance including wide impedance matching, high gain as well as aperture efficiency, they are usually equipped with large SIW (parallel) based feeding network, which consists of plenty of SIW-based power dividers and long SIW lines. Therefore, the loss of the feed network is nonnegligible, which will eventually give rise to the drop of the antenna radiation efficiency. Compared with these works, the proposed design exhibits two advantages. Firstly, the proposed array shows higher radiation efficiency because it uses less power dividers by taking advantages of the high-order-mode cavity excitation. More importantly, the proposed array can be extended to larger array design, such as $8 \times 8$ and $16 \times 16$-element array using even higher resonant modes without sacrificing the radiation efficiency much. Secondly, the proposed array uses fewer vias compared with the design in [30]. This reduces the cost of the antenna. Moreover, at 60-GHz, the effect of the fabrication tolerance on antenna performance should not be neglected because of the very small wavelengths. The fabrication tolerance will affect the antenna performance including...
matching, gain as well as efficiency. Therefore, the proposed array that uses fewer vias can generally have more stable performance. With these good features, the proposed cavity-backed patch antenna array can be used in the future 60-GHz communication systems.

**IV. CONCLUSION**

In summary, a wideband high gain cavity-backed patch antenna array is proposed and demonstrated in this paper. By optimizing the geometry of the radiating patch and the cavity, a very uniform E-field distribution at the antenna aperture is achieved, resulting in the high array aperture efficiency and thus the gain. Taking advantages of the higher-order substrate integrated cavity excitation, the elements of the array are efficiently fed with the same amplitude and phase with a simplified feeding network. Measured results show the antenna bandwidth is from 56 to 63.1-GHz (16.1%) with peak gain reaching 21.4 dBi. The radiation patterns of the array are very stable over the entire frequency band and the cross-polarizations are as low as $-30$ dB.

**ACKNOWLEDGMENT**

(Jianfeng Zhu and Chen-Hao Chu contributed equally to this work.)

**REFERENCES**


YANG YANG (S’11–M’14–SM’17) was born in Inner Mongolia, China. He received the Ph.D. degree from Monash University, Melbourne, VIC, Australia, in 2013. From 2012 to 2015, he was an Asia-Pacific GSP Engineer with Rain Bird, Deer Park, VIC, Australia. From 2015 to 2016, he served as a Senior Research Associate with the Department of Engineering, Macquarie University, Sydney, NSW, Australia. In 2016, he joined the State Key Laboratory of Millimeter Waves, City University of Hong Kong, Hong Kong, as a Research Fellow. In 2016, he was involved in the National Basic Research Program of China (973 Program) and appointed as an Honorary Research Fellow with the Shenzhen Institute, City University of Hong Kong. In 2016, he joined the University of Technology Sydney, Sydney, NSW, Australia, as a Lecturer. His current research interests include radio frequency integrated circuits, microwave and millimeter-wave circuits and systems, reconfigurable antennas, wearable antennas, and wearable sensing devices and technologies. He was a recipient of the Global GSP Success Award in 2014.

SHUFANG LI (SM’09) received the Ph.D. degree from the Department of Electrical Engineering, Tsinghua University, Beijing, China, in 1997. She is currently the Director of the Ubiquitous Electromagnetic Environment Center of Education Ministry, China, and the Director of the Joint Lab, State Radio Monitoring Center, Beijing University of Posts and Telecommunications, China. She has published 100s of papers interior and overseas, and several textbooks, translation works, and patents. Her research interests include the theory and design technology of radio frequency circuits in wireless communication, EMI/EMC, and simulation technology and optimization for radiation interfere on high-speed digital circuit. She received the Young Scientists Reward sponsored by the International Union of Radio Science. She is the Guest Editor of the IEEE TRANSACTIONS ON ELECTROMAGNETIC COMPATIBILITY.